
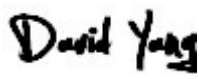







Test Report issued under the responsibility of:



<b>TEST REPORT</b> <b>IEC 62841-1</b> <b>Electric Motor-Operated Hand-Held Tools,</b> <b>Transportable Tools and Lawn and Garden Machinery – Safety</b>	
<b>Report Number.....</b>	<b>6138614.51QS</b>
<b>Date of issue.....</b>	<b>2022.12.23</b>
<b>Total number of pages .....</b>	<b>16</b>
<b>Name of Testing Laboratory preparing the Report .....</b>	DEKRA Testing and Certification (Shanghai) Ltd. 3F #250 Jiangchangan Road Building 16, Headquarter Economy Park Shibe Hi-Tech Park, Jing'an District Shanghai 200436 CHINA
<b>Applicant's name .....</b>	LEE YEONG INDUSTRIAL CO., LTD.
<b>Address.....</b>	No.2, Kejia Rd., Douliu City, Yunlin County 64057, Taiwan
<b>Test specification:</b>	
<b>Standard .....</b>	Sub-clause 18.8 of IEC 62841-1: 2014; EN 62841-1:2015 IEC 62841-2-3:2020 EN IEC 62841-2-3:2021+A11:2021
<b>Test procedure .....</b>	SCF assessment
<b>Non-standard test method .....</b>	N/A
<b>TRF template used.....</b>	IECEE OD-2020-F1:2020, Ed.1.3
<b>Test Report Form No. ....</b>	IEC 62841_SCF_Assessment_1A
<b>Test Report Form(s) Originator ....</b>	DEKRA Testing and Certification (Shanghai) Ltd.
<b>Master TRF .....</b>	2022-02-23
<b>General disclaimer:</b>	
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<b>Test item description</b> ..... :	Cordless Straight Grinder	
<b>Trade Mark</b> ..... :	AGP	
<b>Manufacturer</b> .....	LEE YEONG INDUSTRIAL CO., LTD. No.2, Kejia Rd., Douliu City, Yunlin County 64057, Taiwan	
<b>Model/Type reference</b> .....	SG6; SG150; SG30; SSG; STRAIGHT-MATE(30A-606); STRAIGHT-MATE(30A-626); RISG6-1500; PT-G10601	
<b>Ratings</b> .....	110-120 Vac; 1400 W 220-240 Vac; 1500 W	
<b>Software version</b> .....	SG6V1_V01	
<b>Hardware version</b> ..... :	SM5_V03	
(PCB)		
<b>Responsible EMC Testing Laboratory (as applicable), testing procedure and testing location(s):</b>		
<b>CB Testing Laboratory:</b>		
<b>Testing location/ address</b> ..... :	DEKRA Testing and Certification (Shanghai) Ltd. 3F #250 Jiangchangsan Road Building 16, Headquarter Economy Park Shibe Hi-Tech Park, Jing'an District Shanghai 200436 CHINA	
<b>Tested by (name, function, signature)</b> ..... :	Xueyan Zhao	
<b>Approved by (name, function, signature)</b> ... :	David Yang	

<b>Test item particulars</b> .....: -	
<b>Possible test case verdicts:</b>	
- test case does not apply to the test object.....: N/A	
- test object does meet the requirement.....: P (Pass)	
- test object does not meet the requirement.....: F (Fail)	
<b>Testing</b> .....:	
<b>Date of receipt of test item</b> .....: 2022.07.17	
<b>Date (s) of performance of tests</b> .....: 2022.07.17-2022.08.17	
<b>General remarks:</b>	
<p>"(See Enclosure #)" refers to additional information appended to the report.          "(See appended table)" refers to a table appended to the report.</p> <p><b>Throughout this report a <input checked="" type="checkbox"/> comma / <input type="checkbox"/> point is used as the decimal separator.</b></p> <p>Required performance levels for applicable safety critical functions are specified by the relevant part of IEC 62841-2, IEC 62841-3 or IEC 62841-4. Typical safety critical functions are indicated in table 18.8.1A.</p> <p>Software used in circuits of programmable devices whose failure would create loss of safety critical function, complied with requirements as in table 18.8.1B according to IEC 60730-1:2010.</p> <p>If safety critical functions are evaluated by using the fault conditions of 18.6.1 and will not result in a loss of any safety critical function or shall place and maintain the tool into a safe state, Software assessment is not applicable. See table 18.6.1 while the fault condition is present.</p>	
<b>Manufacturer's Declaration per sub-clause 4.2.5 of IEC 60300-2:</b>	
The application for obtaining a CB Test Certificate includes more than one factory location and a declaration from the Manufacturer stating that the sample(s) submitted for evaluation is (are) representative of the products from each factory has been provided .....	<input type="checkbox"/> <b>Yes</b> <input checked="" type="checkbox"/> <b>Not applicable</b>
<b>When differences exist, they shall be identified in the General product information section.</b>	
<b>Name and address of factory (ies)</b> .....: LEE YEONG INDUSTRIAL CO., LTD. NO.2, KEJIA RD, DOULIU CITY, YUNLIN COUNGY, TAIWAN	
<b>General product information and other remarks:</b>	
Cordless Straight Grinder: Model: SG6; SG150; SG30; SSG; STRAIGHT-MATE(30A-606); STRAIGHT-MATE(30A-626); RISG6-1500; PT-G10601	
<b>Description of Safety Critical Functions (SCF), if any:</b>	
1. Prevent exceeding thermal limits as in Clause 18	

18.8	<b>Electronic circuits providing safety critical functions (SCF)</b>		—
18.8.1	Electronic circuits providing SCF are reliable and not susceptible to loss of SCF due to electro-magnetic environmental stresses	See documents:  SG6-2_CS-20220928  SG6-2_Dips-20220928  SG6-2_EFT_20220928  SG6-2_ESD_20220928  SG6-2_Surge_20220928	P
	No SCF lost after tests of 18.8.2 to 18.8.6 for circuits with no internal clock frequency or oscillator frequency > 15 MHz	> 15 MHz (4 MHz)	N/A
	No SCF lost after tests of 18.8.2 to 18.8.7 for other electronic circuits	For Mains tools: No SCF lost after tests of 18.8.2 to 18.8.7.	P
	Test voltage was rated voltage or the mean value of the rated voltage range..... :	110-120 Vac; 220-240 Vac	P
	Difference between upper and lower limit of rated voltage range > 20 % of its mean value, test at both upper and lower limits of the rated voltage range .. :		N/A
	After evaluation using 18.6.1, no loss of any SCF or tool in a safe state under any present fault condition.		N/A
	Concept of 18.6.1 not appropriate, reliability evaluated using ISO 13849-1.	1.Prevent exceeding thermal limits as in Clause 18	P
	Required performance levels .....	See Table 18.8.1A	P
	If only MTTF <sub>d</sub> is applied to achieve the required PL: MTTF <sub>d</sub> is 5/20/50 years for PL = a/b/c		P
	Software used in circuits of programmable devices whose failure would create loss of safety critical function, complied with software class B requirements as in H.11.12.3 of IEC 60730-1:2010	See Table 18.8.1B	P
	In the case where software class B is realized by single channel with periodic self-test, an acceptable period is regarded as either after each activation of the power switch or a maximum of 5 min.		P
	Class B realized by single channel, periodic self-test either after each activation of the power switch or at least every maximum 5 min		P
	H.11.12.3.4.1 applicable for SCF with a PL ≥ c		P
18.8.2	Electrostatic discharges as in IEC 61000-4-2:2008 applied to tool, test level 4 used for air discharge and test level 3 for contact discharge, ten / ten discharges having a positive / negative polarity applied		P
18.8.3	Fast transient bursts as in IEC 61000-4-4:2012 applied to tool, test level 3 used. Repetition frequency 5 kHz for 2 min / 2 min with a positive / negative polarity		P

18.8.4	Voltage surges as in IEC 61000-4-5:2005 applied to power supply terminals, five positive impulses and five negative impulses applied at the selected points		P
	Test level 3 applied for line-to-line coupling mode, a generator with 2 $\Omega$ source impedance being		P
	Test level 4 applied for line-to-earth coupling mode, a generator with 12 $\Omega$ source impedance being		P
	Tools has surge arresters incorporating spark gaps, test was repeated at 95 % of the flashover voltage		P
18.8.5	Injected currents as in IEC 61000-4-6:2008 applied to tool, test level 3 applicable, all frequencies between 0,15 MHz to 230 MHz covered		P
18.8.6	Class 3 voltage dips and interruptions in accordance with IEC 61000-4-11:2004 applied to tool		P
	Values of Tables 1 and 2 of IEC 61000-4-11:2004 were applied at zero crossing of the supply voltage		P
18.8.7	Radiated fields in accordance with IEC 61000-4-3:2010 applied to tool, test level 3 applicable		N/A
	Frequency ranges 80 MHz to 1 000 MHz tested		N/A

<b>18.6.1</b>	<b>TABLE: Fault Condition Tests</b>		
	Ambient temperature (°C) .....	20°	—
	Fuse-link Current (A)	—	—

The test method is to open or short circuit the safety critical functional related electronic components, the test results all passed (normal operation with no loss of SCF or no operation/ safe state).

18.8.1A	<b>TABLE: Performance levels of Safety Critical Functions</b>			
Type and purpose of SCF	Min. PL determined based on: <sup>1,2</sup>	Min. PL	Actual PL	
Prevent exceeding thermal limits as in Clause 18	a	a	a	
Supplementary Information: <sup>1</sup> Relevant part of IEC 62841-2, IEC 62841-3 or IEC 62841-4 or; if no such part existent, ISO 13849-1 using Annex E as a guide <sup>2</sup> For safety critical functions not listed in Table 4 of IEC 62841-1 and provided by electronic circuits, PL values were determined using the methods of ISO 13849-1.				

<b>18.8.1B</b>	<b>TABLE: Software in Safety Critical Functions</b>		—
H.11.12.3 from IEC 60730-1:2010			
H.11.12.3	Measures to avoid errors		—
H.11.12.3.1	For controls with software Class B or C the V-model for the software life cycle was applied		P
	Measures used for software class C are inherently acceptable for software class B		N/A
	Other methods applied if they incorporate disciplined and structured processes including design and test phases .....		N/A
H.11.12.3.2	Specification		—
H.11.12.3.2.1	Software safety requirements		—
H.11.12.3.2.1.1	The specification of the software safety requirements includes:		—
	<ul style="list-style-type: none"> <li>• A description of each safety related function to be implemented, including its response time(s): <ul style="list-style-type: none"> <li>○ functions related to the application including their related software classes</li> <li>○ functions related to the detection, annunciation and management of software or hardware faults</li> </ul> </li> </ul>		P
	<ul style="list-style-type: none"> <li>• A description of interfaces between software and hardware</li> </ul>		P
	<ul style="list-style-type: none"> <li>• A description of interfaces between any safety and non-safety related functions</li> </ul>		P
H.11.12.3.2.2	Software architecture		—
H.11.12.3.2.2.1	The description of software architecture shall include the following aspects:		—
	<ul style="list-style-type: none"> <li>• Techniques and measures to control software faults/errors (refer to H.11.12.2)</li> </ul>		P
	<ul style="list-style-type: none"> <li>• Interactions between hardware and software</li> </ul>		P
	<ul style="list-style-type: none"> <li>• Partitioning into modules and their allocation to the specified safety functions</li> </ul>		P
	<ul style="list-style-type: none"> <li>• Hierarchy and call structure of the modules (control flow)</li> </ul>		P
	<ul style="list-style-type: none"> <li>• Interrupt handling</li> </ul>		P
	<ul style="list-style-type: none"> <li>• Data flow and restrictions on data access</li> </ul>		P
	<ul style="list-style-type: none"> <li>• Architecture and storage of data</li> </ul>		P
	<ul style="list-style-type: none"> <li>• Time based dependencies of sequences and data</li> </ul>		P

H.11.12.3.2.2	The architecture specification was verified against the specification of the software safety requirements by static analysis. Acceptable methods are:	—
	• Control flow analysis	P
	• Data flow analysis	P
	• Walk-throughs / design reviews	P
H.11.12.3.2.3.1	Based on the architecture design, software is suitably refined into modules. Software module design and coding are implemented in a way that is traceable to the software architecture and requirements	P
H.11.12.3.2.3.2	Software code is structured	P
H.11.12.3.2.3.3	Coded software is verified against the module specification, and the module specification is verified against the architecture specification by static analysis	P
H.11.12.3.2.4	Design and coding standards	—
	Program design and coding standards is consequently used during software design and maintenance	P
	Coding standards specify programming practice, proscribe unsafe language features, and specify procedures for source code documentation as well as for data naming conventions	P
H.11.12.3.3	Testing	—
H.11.12.3.3.1	Module design (software system design, software module design and coding)	—
H.11.12.3.3.1.1	A test concept with suitable test cases is defined based on the module design specification.	P
H.11.12.3.3.1.2	Each software module is tested as specified within the test concept	P
H.11.12.3.3.1.3	Test cases, test data and test results are documented	P
H.11.12.3.3.1.4	Code verification of a software module by static means includes such techniques as software inspections, walk-throughs, static analysis and formal proof	P
	Code verification of a software module by dynamic means includes functional testing, white-box testing and statistical testing	P
H.11.12.3.3.2	Software integration testing	P
H.11.12.3.3.2.1	A test concept with suitable test cases is defined based on the architecture design specification	P



H.11.12.3.3.2.2	The software is tested as specified within the test concept		P
H.11.12.3.3.2.3	Test cases, test data and test results are documented		P
H.11.12.3.3.3	Software validation		—
H.11.12.3.3.3.1	A validation concept with suitable test cases is defined based on the software safety requirements specification		P
H.11.12.3.3.3.2	The software is validated with reference to the requirements of the software safety requirements specification as specified within the validation concept.		P
	The software is exercised by simulation or stimulation of:		P
	• input signals present during normal operation		P
	• anticipated occurrences		P
	• undesired conditions requiring system action		P
H.11.12.3.3.3.4	Test cases, test data and test results are documented		P
H.11.12.3.4	Other Items		—
H.11.12.3.4.1	Tools, programming languages are assumed to be suitable if they comply with "increased confidence from use" according to IEC 61508-7, C.4.4	Only applicable for SCF with $PL \geq c$	P
H.11.12.3.4.2	Management of software versions: All versions are uniquely identified for traceability		P
H.11.12.3.4.3	Software modification		—
H.11.12.3.4.3.1	Software modifications are based on a modification request which details the following:		—
	• the hazards which may be affected		N/A
	• the proposed change		N/A
	• the reasons for change		N/A
H.11.12.3.4.3.2	An analysis is carried out to determine the impact of the proposed modification on functional safety.		N/A
H.11.12.3.4.3.3	A detailed specification for the modification is generated including the necessary activities for verification and validation, such as a definition of suitable test cases		N/A
H.11.12.3.4.3.4	The modification are carried out as planned		N/A
H.11.12.3.4.3.5	The assessment of the modification is carried out based on the specified verification and validation activities. This may include:		N/A

	<ul style="list-style-type: none"> <li>• a reverification of changed software modules</li> </ul>		N/A
	<ul style="list-style-type: none"> <li>• a reverification of affected software modules</li> </ul>		N/A
	<ul style="list-style-type: none"> <li>• a revalidation of the complete system</li> </ul>		N/A
H.11.12.3. 4.3.6	All details of modification activities are documented		N/A
H.11.12.3. 5	For class C control functions: One of the combinations (a–p) of analytical measures given in the columns of table H.9 is used during hardware development .....	Measures to avoid errors for class C not required	N/A

## **Annex 1 Description of Test Object**

The product under evaluation is the safety critical function s of model:

SG6; SG150; SG30; SSG; STRAIGHT-MATE(30A-606); STRAIGHT-MATE(30A-626); RISG6-1500;  
PT-G10601 manufactured by LEE YEONG INDUSTRIAL CO., LTD.

The following safety critical functions are evaluated in this report.

SCF1: Prevent exceeding thermal limits as in Clause 18

Over current protective function is provided to meet this requirement. According to the above table 18.8.1A,  
PLr = a.

**Annex 2 Achieved Performance Level--Calculation of MTTFd**

Component	Reliability Reference	No.	Units n	MTTFd Typical years	n/MTTFd Typical 1/year
Resistor-Carbon Film	ISO 13849	R1, R3, R16	3	114155	0.0000263
Capacitor-Ceramics	ISO 13849	C2, C4, C5, C7, C9, C8, C11, CT1	8	45662	0.0001752
Standard, no power	ISO 13849	XC1, XC2, XC3	3	114115	0.0000263
Aluminium electrolytic	ISO 13849	C1, C3, C6	3	45662	0.0000657
Rectifier diodes	ISO 13849	D1, D2, D3, D4, D5	5	228311	0.0000219
Zener diode Ptot < 1 W	ISO 13849	ZD1	1	228311	0.0000044
Low frequency inductors	ISO 13849	T1	1	45662	0.0000219
Transistor-Bipolar, universal	ISO 13849	Q1, Q2	2	76104	0.0000263
MCU	supplier	IC1	1	10	0.1000000
Triac	ISO 13849	TR1	1	3044	0.0003285
					0.1006964
MTTFd (years)Total:					9.930837202

Table: MTTFd 1

Component	Reliability Reference	No.	Units n	MTTFd Typical years	n/MTTFd Typical 1/year
Resistor-Carbon Film	ISO 13849	R1, R9, R16	3	114155	0.0000263
Capacitor-Ceramics	ISO 13849	C2, C4, C5, C7, C8, C9, C11, C12	8	45662	0.0001752
Standard, no power	ISO 13849	C10, XC1, XC2, YC1, YC2	5	114115	0.0000438
Aluminium electrolytic	ISO 13849	C1, C3, C6	3	45662	0.0000657
Rectifier diodes	ISO 13849	D1, D2, D3, D4, D5	5	228311	0.0000219
Zener diode Ptot < 1 W	ISO 13849	ZD1	1	228311	0.0000044
Transistor-Bipolar, universal	ISO 13849	Q1, Q2	2	76104	0.0000263
MCU	supplier	IC1	1	10	0.1000000
Triac	ISO 13849	TR1	1	3044	0.0003285
					0.1006921
MTTFd (years)Total:					9.931268578

Table: MTTFd 2

The calculated overall MTTF<sub>d</sub> value over **5 years**. According to Cl. 4.6 of EN ISO 13849-1, the circuitry design can meet this requirement.

### Annex 3 Description of Safety Software

#### System Description

System diagram

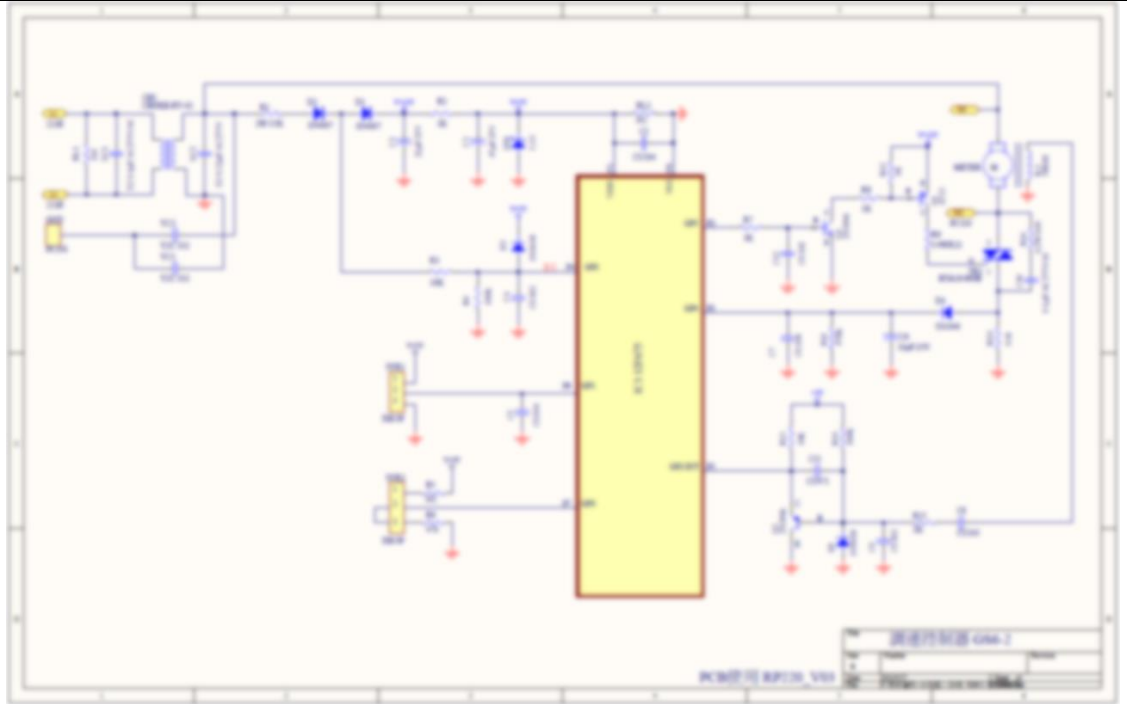


Figure1: Hardware Structure Diagram

Description of SCF- Prevent exceeding thermal limits as in Clause 18:

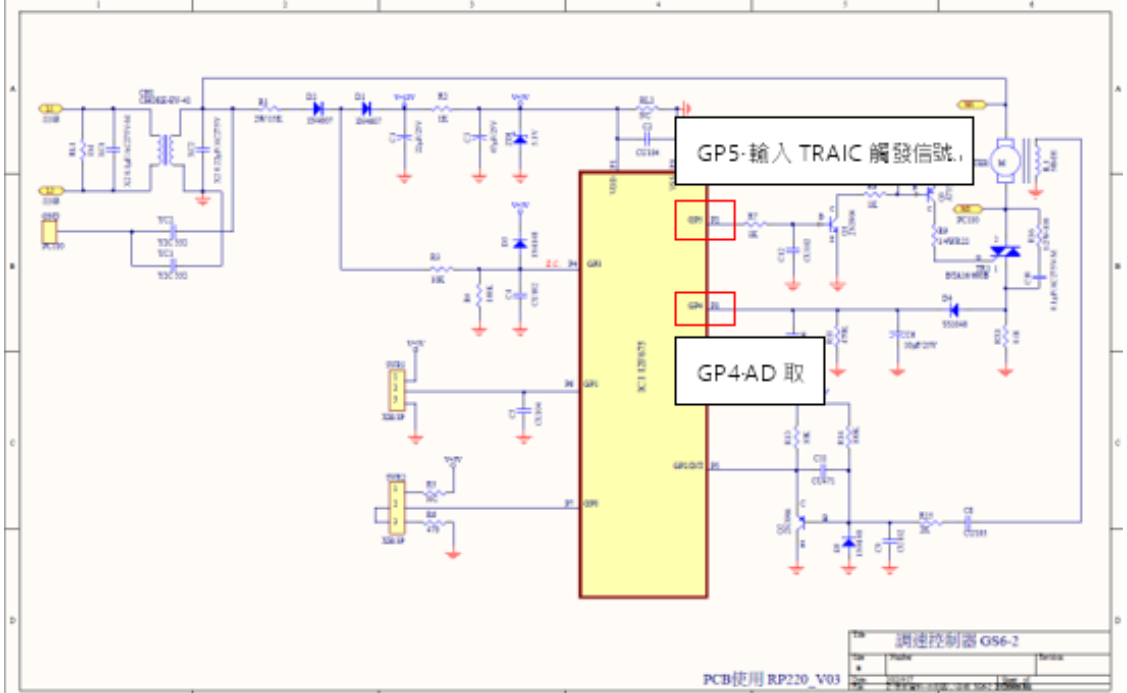


Figure 2: Schematic of Overcurrent

Overcurrent protection:

IC 12F675 and hardware circuit implement the overcurrent protection, GP5/Pin P2 send turn on/off signal, drive the TR1, the current will be sampled by GP4/Pin P3, if the current over the threshold value, MCU will shut the output signal.

**Software Architecture Design**

Software Architecture

Firstly, system initialization is performed, including initializing MCU peripheral, clear watchdog, etc. Then, sampling is enabled. The tool starts working only when the switch is determined pressed on, otherwise, it is kept not working. When the system works in the normal working state, motor current are periodically checked. If over current is judged, over current protective function is executed, the tool is stopped working. If the switch is determined released, the tool is stopped.

Flowchart:

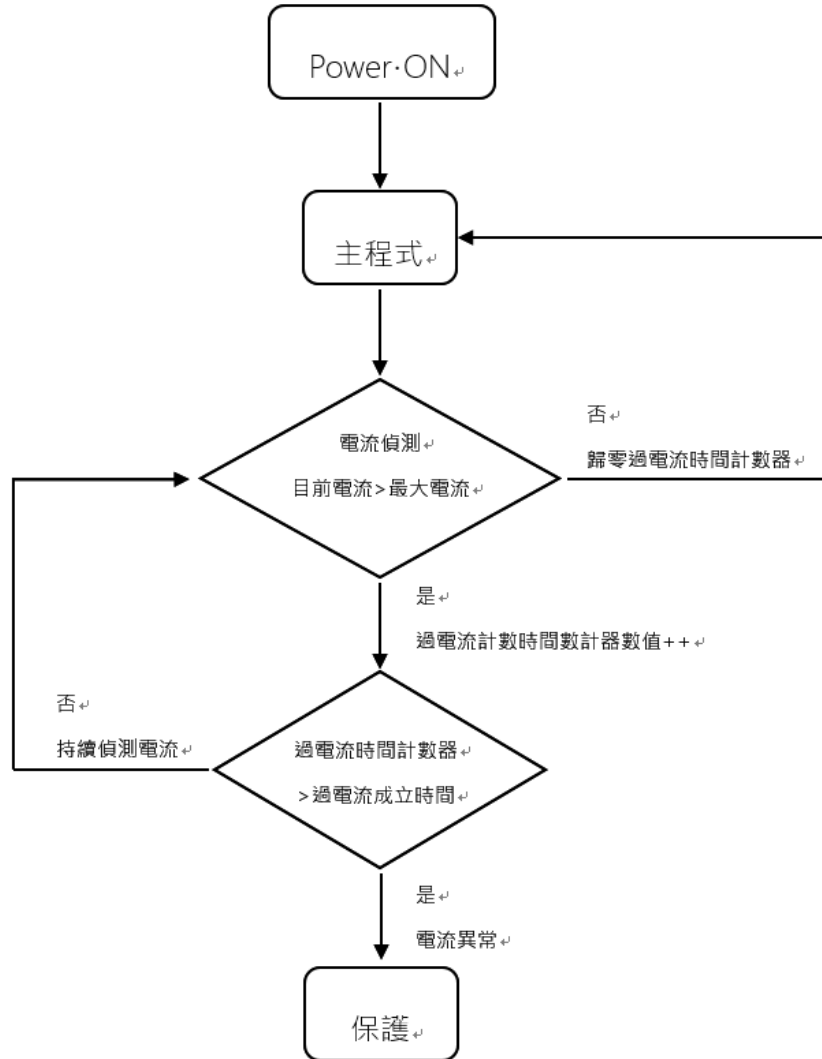


Figure 3: Software Architecture

The Overcurrent threshold value:

- Current overload point 1: 8.5A is established for 20 consecutive seconds.
- Current overload point 2: 9.5 A is valid for 3 consecutive seconds.

Note: No current is detected within 3 seconds of motor operation.

Architecture Verification	<p>A control flow analysis is used as the static analysis approach to verify the architecture against the software safety requirements.</p> <p>The following aspects of the drop prevention function have been taken into account:</p> <ul style="list-style-type: none"> <li>- Proper flow of the code</li> <li>- Code structure analysis</li> <li>- Data processing</li> </ul> <p>The code must meet the following criteria:</p> <ol style="list-style-type: none"> <li>1. For the control flow of the code: <ul style="list-style-type: none"> <li>- Meet the predefined design in terms of code sequence</li> <li>- No uncontrolled loops</li> <li>- No unused code and variables/objects</li> <li>- No unexpected code (paths/flow)</li> </ul> </li> <li>2. For code structure: <ul style="list-style-type: none"> <li>- No unexpected/unintended outputs</li> <li>- No unexpected actions/responses</li> </ul> </li> <li>3. For data processing: <ul style="list-style-type: none"> <li>- All safety related variables must be in a certain range</li> <li>- All safety related variables must be initialized before using</li> <li>- All safety related variables must be correctly used after definition (to avoid variables unused or improperly used)</li> </ul> </li> </ol> <p>After verification, all the code meets these criteria. The software developed has the same architecture design as the software specification, see manufacturer's document "SG6-2_Overcurrent protection work description_20220929".</p>
<b>Module Design and Coding</b>	
Module Design and Coding Techniques	<p>Following techniques are used to assure the modular design and coding of the software:</p> <ul style="list-style-type: none"> <li>- Size of functional module is limited to be as small as possible</li> <li>- Each software module refers to a single object</li> <li>- Values get from ADC out of a certain range is illegal</li> <li>- Divide by zero situations is handled individually for each division</li> <li>- During initialization, RAM memory is erased (0 value) to assure a known value before executing of any code</li> </ul> <p>Following techniques are used to keep the code structured:</p> <ul style="list-style-type: none"> <li>- An internal coding standard is used</li> <li>- No dynamic variables used and no dynamic memory allocation involved</li> <li>- Interrupts are fixed at design level</li> <li>- Recursion is not used</li> <li>- No unconditional jump are used</li> </ul>
Module Verification	<p>A control flow analysis is used as the static analysis approach to verify the module against the module techniques specification. After verification, all the code meets these criteria. The software module developed has the same architecture design as the software module specification, see manufacturer's document "SG6-2_Overcurrent protection work description_20220929".</p>
<b>Validation</b>	
Validation of SCF- Prevent exceeding thermal limits as in Clause 18:	<p>When the tool ran at overload, during and after the test, the tool did not exceed thermal limits. see manufacturer's document "SG6-2_Validation test 20220929".</p>

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